



SV COLLEGE OF ENGINEERING

(AUTONOMOUS)

Karakambadi Road, Tirupati-517507

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EXAMINATION BRANCH

M.Tech II Semester I Internal Examinations July-2023 TIME TABLE

AN : 02.00 p.m. to 04.00 p.m

Date/Day	VLSI Design (VLSID)	Computer Science and Engineering (CSE)
17-07-2023 (Monday)	CMOS Mixed Signal Design	Advanced Algorithms
18-07-2023 (Tuesday)	Physical Design Automation	Advanced Computer Architecture
19-07-2023 (Wednesday)	MEMS System Design	Network security
20-07-2023 (Thursday)	Low power VLSI Design	Human Computer Interaction
21-07-2023 (Friday)	Entrepreneurship & Incubation	Enterprise Resource planning


CONTROLLER OF EXAMINATIONS


PRINCIPAL

Copy To:

1. Additional Controller of Examinations.
2. HOD (ECE & CSE) with a request to circulate among Staff and students and make arrangements to send all the question papers (soft copy & hard copy) to the Exam cell on or before 14-07-2023, 04.00 P.M.
3. All Notice Boards, T & P Cell, I/C Security, I/C Hostel, I/C Transport, I/C Canteen and AO for information.

07/07/2023